



SLIMbus®

Protocol Analyzer

KEY FEATURES

- Decode and analyze every bit fields
- Multiple levels of analysis
- Protocol error flagging
- Powerful export functions
- Ergonomic & intuitive interface
- Full trace recording
- Real time display of SMC traffic



HARDWARE CHARACTERISTICS

- USB powered and controlled
- Use 1.2V, 1.8V and 3.3V SLIMbus levels
- Shared with the Traffic Generator
- Full speed acquisition (28 Mbps)

FIELD OF APPLICABILITY

- Simulation benches (through VCD files import)
- FPGA IP validation (with capture hardware)
- Silicon validation (with capture hardware)
- Application software debug (with capture hardware)

**In use since 2007, The LnK Protocol Analyzer is an industry reference
It is so far the only vector of SLIMbus chipset interoperability**

